IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Josef Halamik et al

FILED: Concurrently Herewith

FOR: POWER SYSTEM INHIBIT METHOD AND DEVICE AND

STRUCTURE THEREFOR

Date: 10/10/2003

DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.56

Honorable Assistant Commissioner for Patents Washington, D.C. 20231

SIR:

It is respectfully requested that the prior art listed on FORM PTO-1449 be considered in the examination of the subject application and made of record therein. A copy of the listed prior art is enclosed herewith.

No representation is made or intended that the listed prior art enclosed herewith is material to patentability of the subject patent application.

No representation is made or intended that a search has been made or that no better prior art than that listed is available.

Respectfully submitted, Josef Halamik et a,l

James J. Stipanuk

Reg. #44,358 Phone: 602-244-4885 Fax: 602-244 3169

P.O. Box 62890 - A700 Phoenix, AZ 85062-2890

Intellectual Property Dept.

ON Semiconductor

Customer

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27255

Form PTO-1449

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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE

STATEMENT (Use Several Sheets if Necessary)

ATTY. DOCKET NO.	SERIAL NO.	
ONS00482		
APPLICANT Josef Halamik et al		
FILING DATE	GROUP	
CONCURRENTLY		

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS													
Examiner Initial	Docum				r				Date	Name	Class	Sub- Class	Filing date (If approp rirate)
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FOREIGN PATENT DOCUMENTS

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	OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.